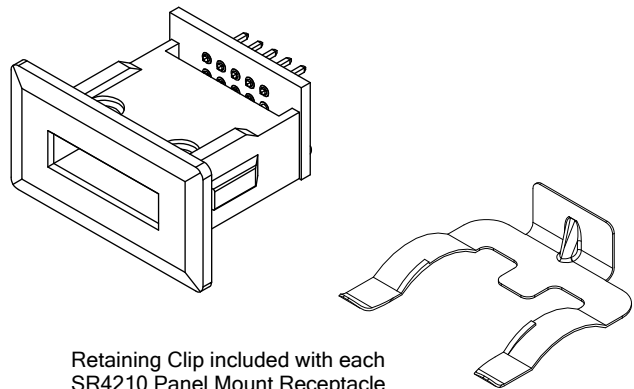


The SR4210 is a panel-mount interface connector for all Datakey Electronics' SlimLine™, Extended SlimLine, NFX, and DFX RUGGEDrive™ Tokens (see token data sheets for more information). The SR4210 Receptacle features corrosion-resistant, gold dot contacts that perform reliably over a wear life of at least 50,000 insertion/removal cycles. The receptacle includes a detent mechanism that gives users tactile confirmation when an inserted token is physically engaged. The SR4210 also contains a Last-On/First-Off (LOFO) contact that may be used to protect the host bus by ensuring that tokens have made secure contact with the receptacle before any signals are transmitted (please contact factory for this option with NFX and DFX Tokens). The SR4210 Receptacle is designed for simple, front-panel mounting in small equipment with limited component space. A metal retainer clip is provided to hold the receptacle to the panel.

Mechanical	
Operating Life	50,000 insertion/removal cycles min.
Insertion Force	400 grams min. / 2 Kgrams max.
Removal Force	300 grams min. / 2 Kgrams max.
Vibration	15 G's (three axes) Non-operating
Electrical (Dielectric Strength)	
Contact Resistance	Beginning of Life: 100 mΩ
	End of Life: 500 mΩ
Environmental	
Storage Temperature	-40°C to +105°C
Operating Temperature	-40°C to +85°C
Relative Humidity	5% to 95% (non-condensing)
Mating Component(s)	
Tokens	All SlimLine, NFX and DFX Tokens
Ordering Information <sup>1</sup>	
SR4210	606-0051-000A



Retaining Clip included with each SR4210 Panel Mount Receptacle.

#### NOTES:

- 1: "A" suffix on part number indicates RoHS compliance.
- 2: Complete Interface Specifications available at [http://www.datakeyelectronics.com/technical\\_inter\\_specs.html](http://www.datakeyelectronics.com/technical_inter_specs.html)
- 3: RST signal used on IIT Series only.

Pin-Out Chart <sup>2</sup>					RUGGEDrive™ Line
PIN #	Microwire	I <sup>2</sup> C	SPI	NFX	DFX (SPI)
Pin 1	NC	NC	NC	NC	NC
Pin 2	Power (V <sub>CC</sub> )	Power (V <sub>CC</sub> )	Power (V <sub>CC</sub> )	Power (V <sub>CC</sub> )	Data Out (DO)
Pin 3	Ground (GND)	Ground (GND)	Ground (GND)	Ground (GND)	/Chip Select (/CS)
Pin 4	Do Not Use	SIZE / RST <sup>3</sup>	Do Not Use	Do Not Use	Ground (GND)
Pin 5	Chip Select (CS)	Do Not Use	/Chip Select (/CS)	/Chip Select (/CS)	Ground (GND)
Pin 6	Data In (DI)	Do Not Use	Data In (SI)	MOSI	Power (V <sub>CC</sub> )
Pin 7	Serial Clock (SK)	Serial Clock (SCL)	Serial Clock (SCK)	Serial Clock (SCK)	Serial Clock (SCLK)
Pin 8	Data Out (DO)	Serial Add/Data (SDA)	Data Out (SO)	MISO	Data In (DI)
Pin 9	Do Not Use	Do Not Use	/Hold	DP (USB)	Reserved
Pin 10	LOFO	LOFO	LOFO	DM (USB)	Reserved

NC = No Connection



